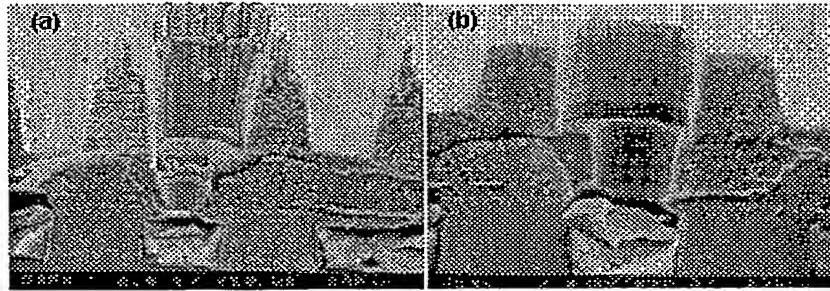


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**FIGURE 1**

Source: Semiconductor International, July 1, 2001

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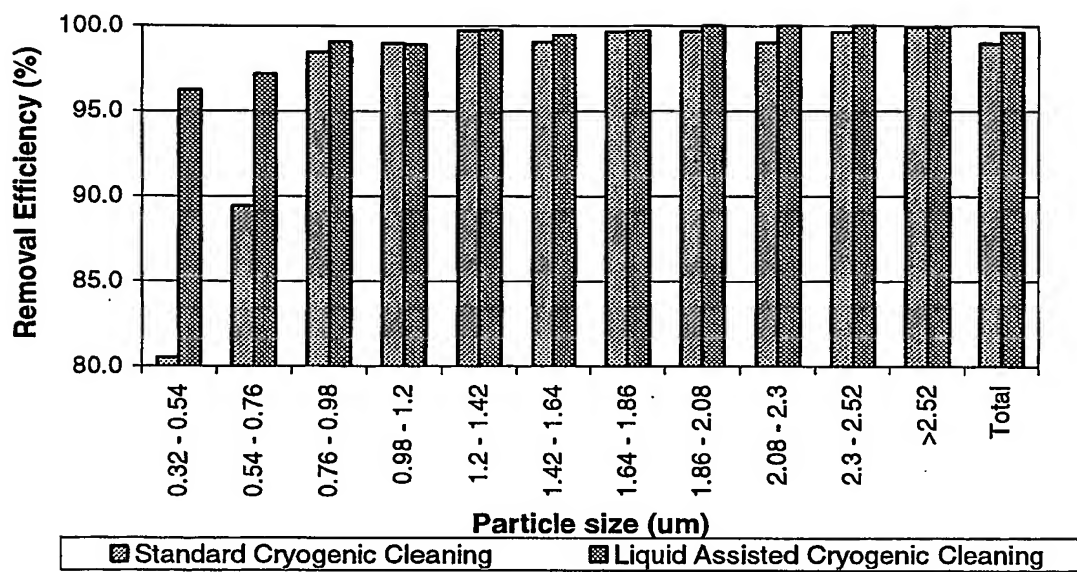


FIGURE 2

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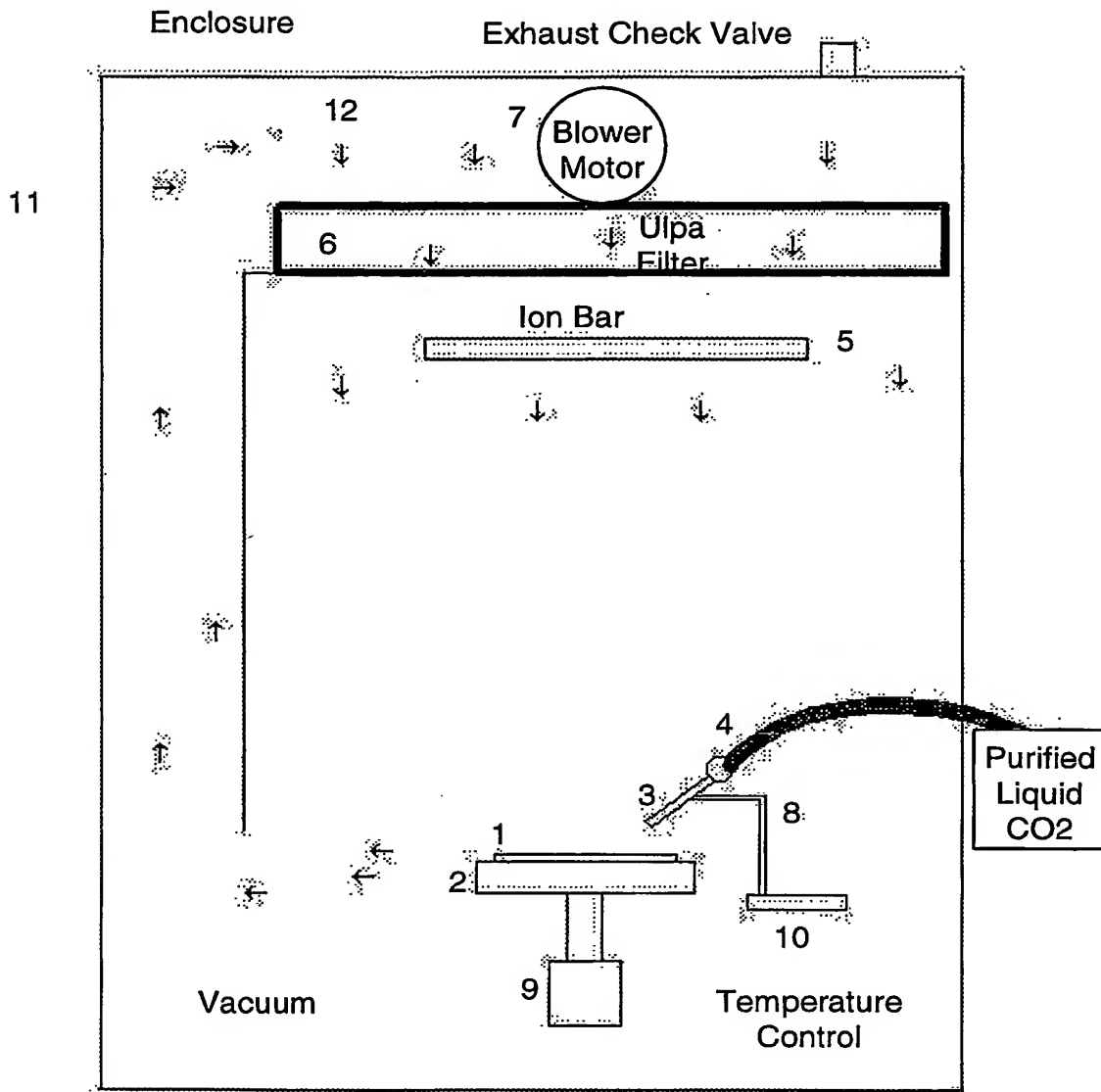


FIGURE 3

BEST AVAILABLE COPY